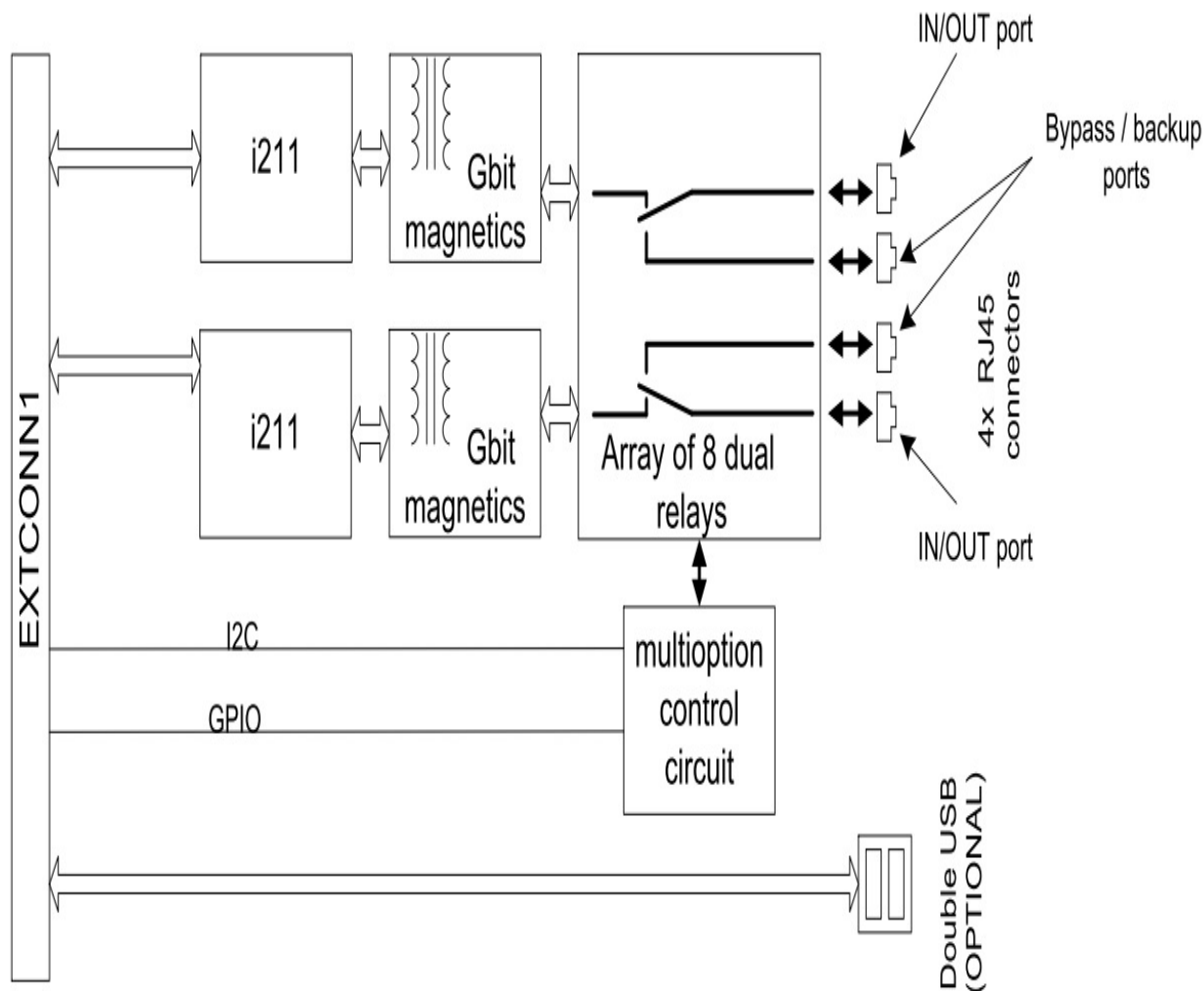


FM-EBP rev 1.0

PAGE	FUNCTION
01	INDEX
02	EXT CONNECTORS
03	USB AND RELAY CONTROL
04	ETHERNET 1
05	ETHERNET 1
06	RELAY
07	POWER & CONTROL



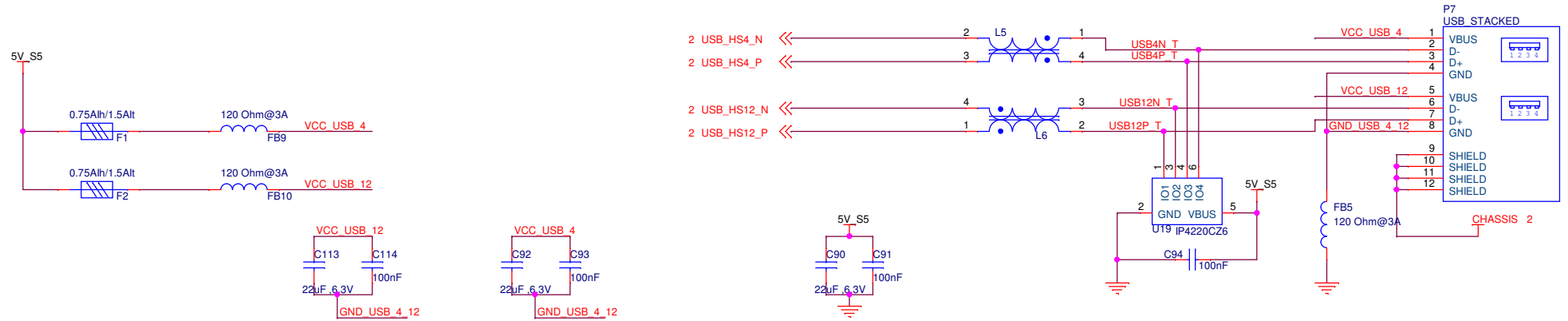
FP1
FP, FM-EBP Rev 1.0

ZZ1
PCB1
PCB, FM-EBP Rev 1.0
PARSER_VERSION_1.0

SMBus address:
0x54 h - EEPROM BOARD INFO.
0x26 h- GPIO CONTROLLER

FACE MODULE FOR:
FITPC & INTENC-PC & MICROSERVER
BYPASS ETHERNET x 2
ETHERNET x 2
USB x 2

USB PORT 4/12



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Nesher 36770, Israel
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Size B Title FM-EBP 03. USB & CONTROL

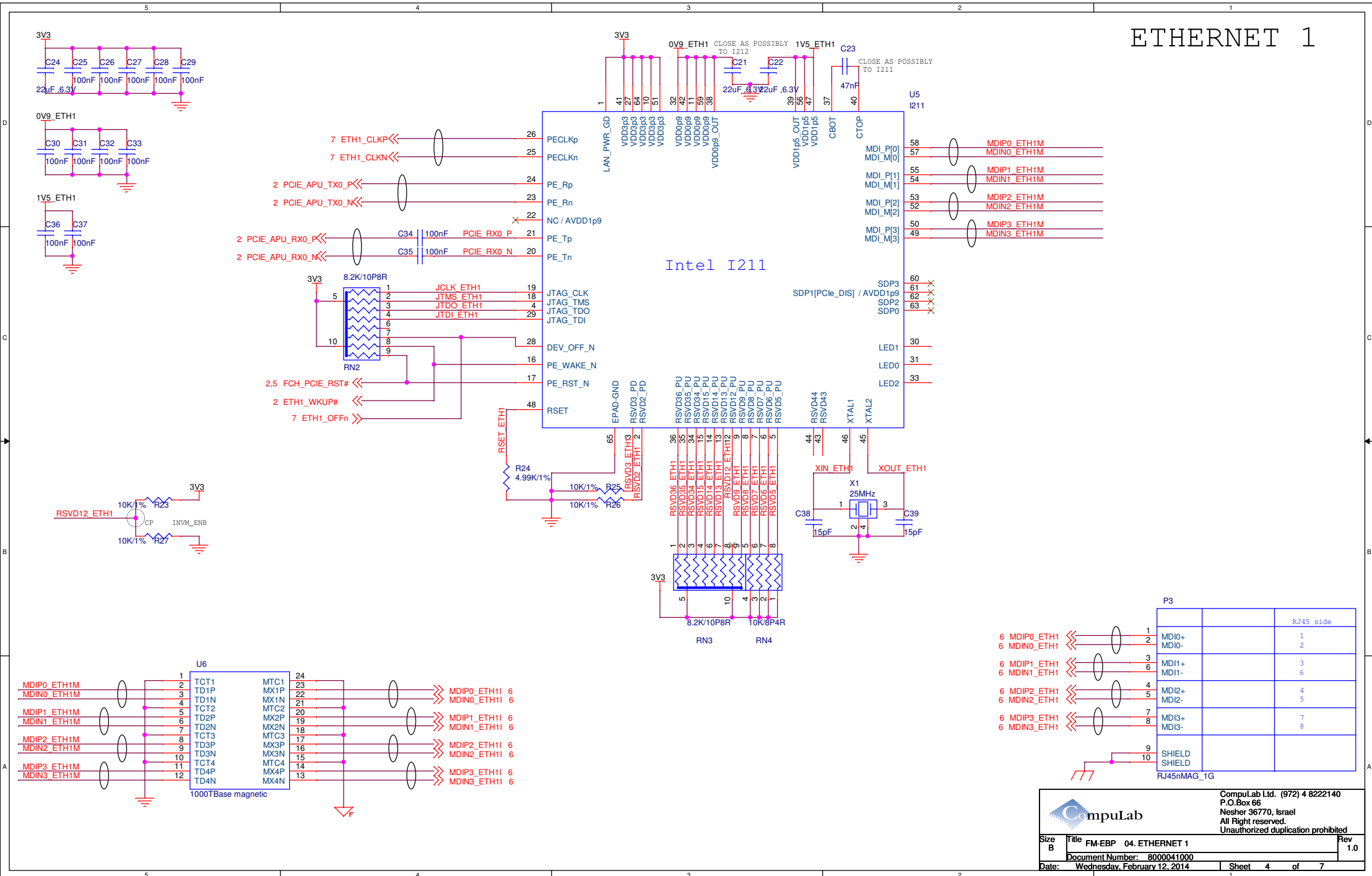
Document Number: 8000041000

Date: Wednesday, February 12, 2014

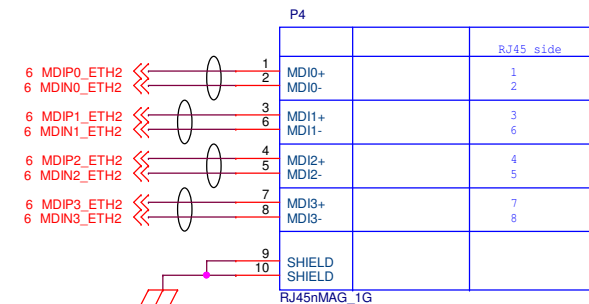
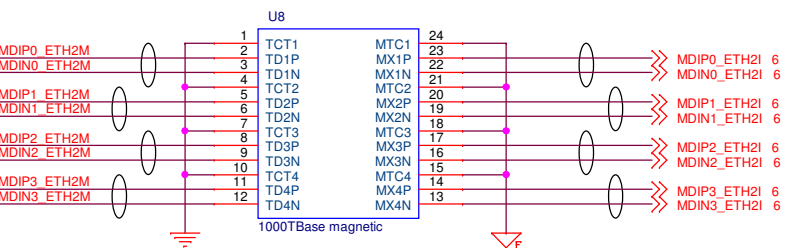
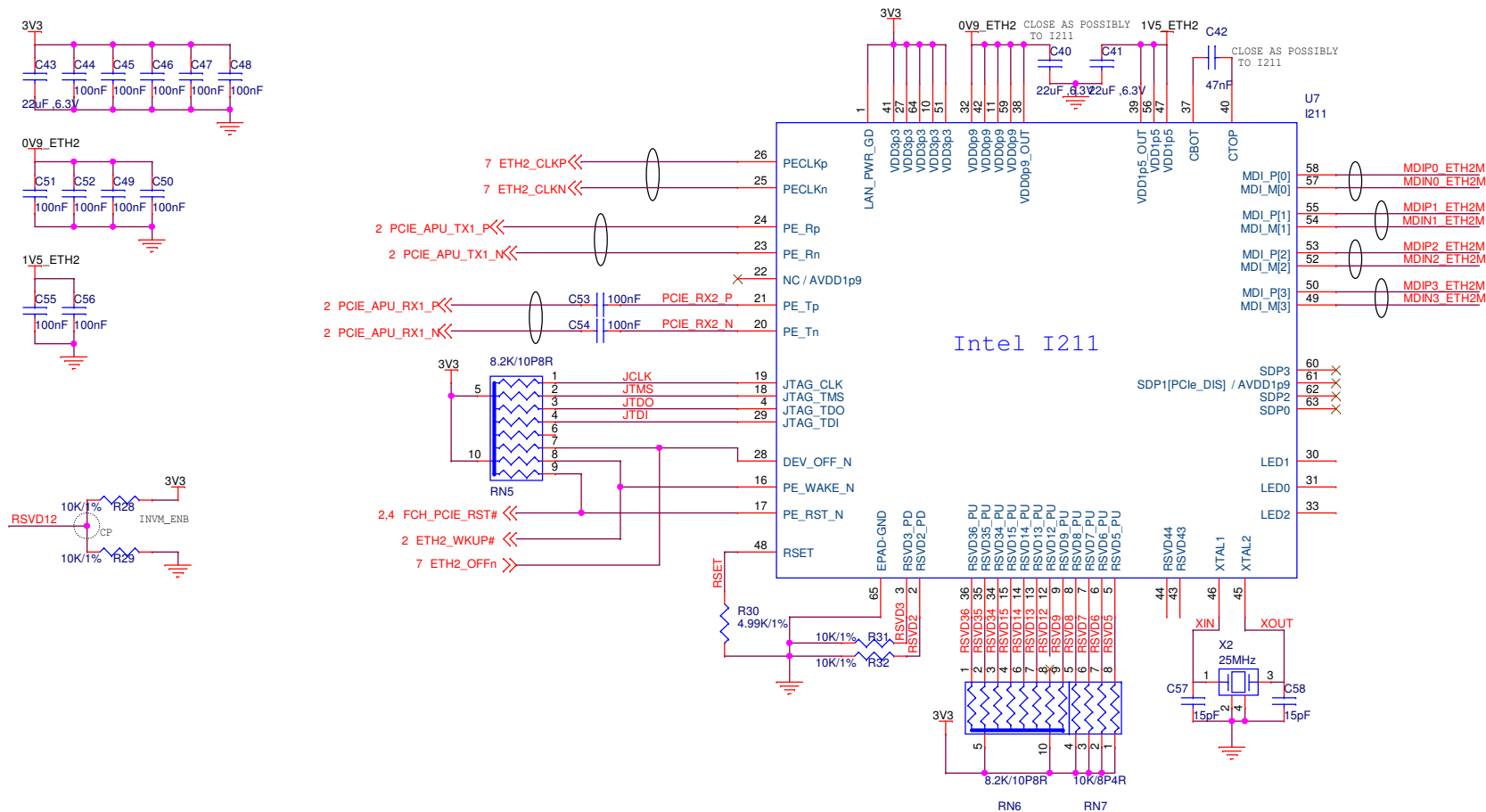
Sheet 3 of 7

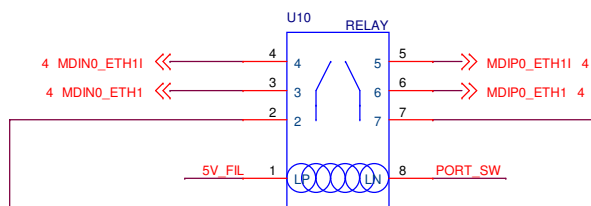
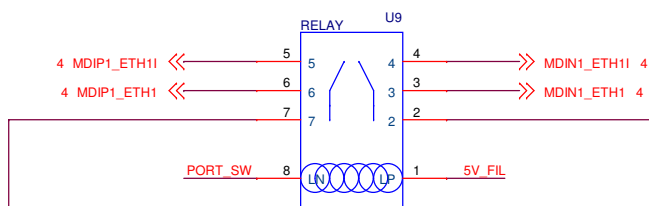
Rev 1.0

ETHERNET 1



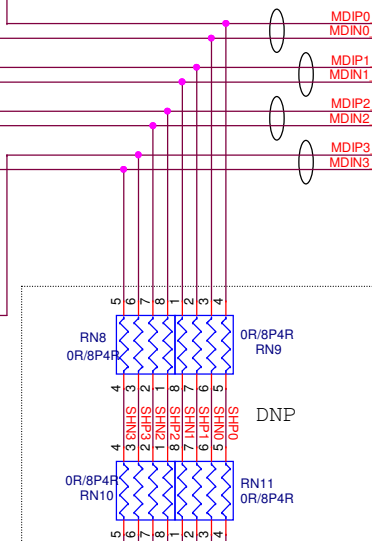
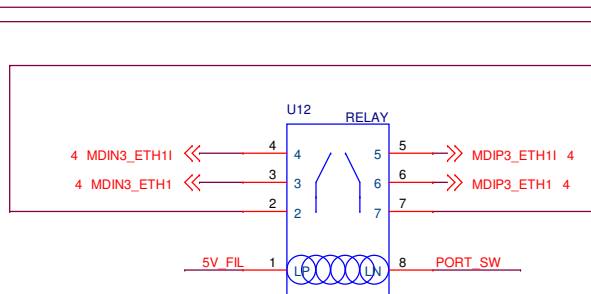
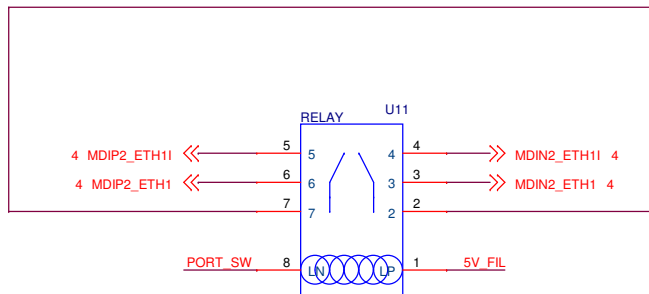
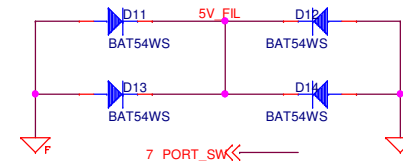
ETHERNET 2



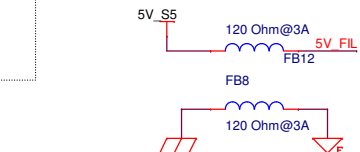
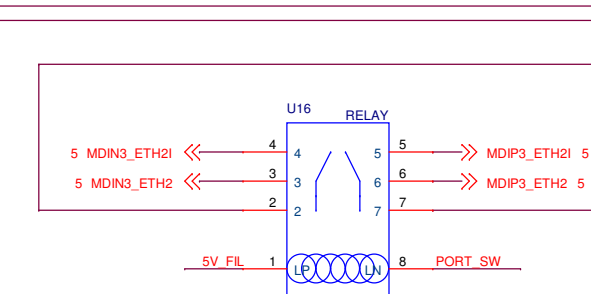
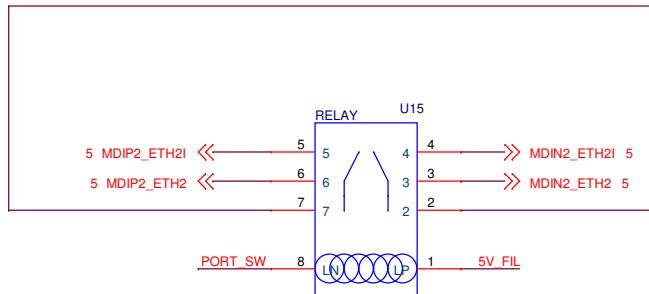
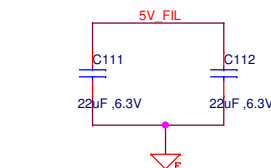
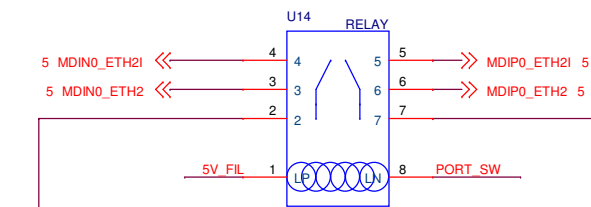
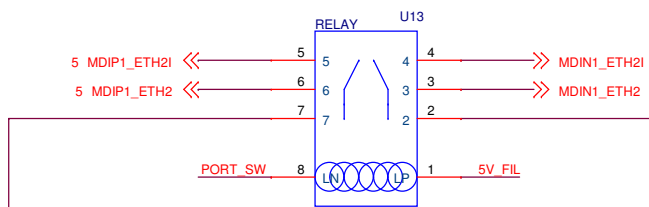


Dissipate the energy from the collapsing magnetic field at deactivation.

LAYOUT:
Place near as possible to relay coil (pin 1,8)

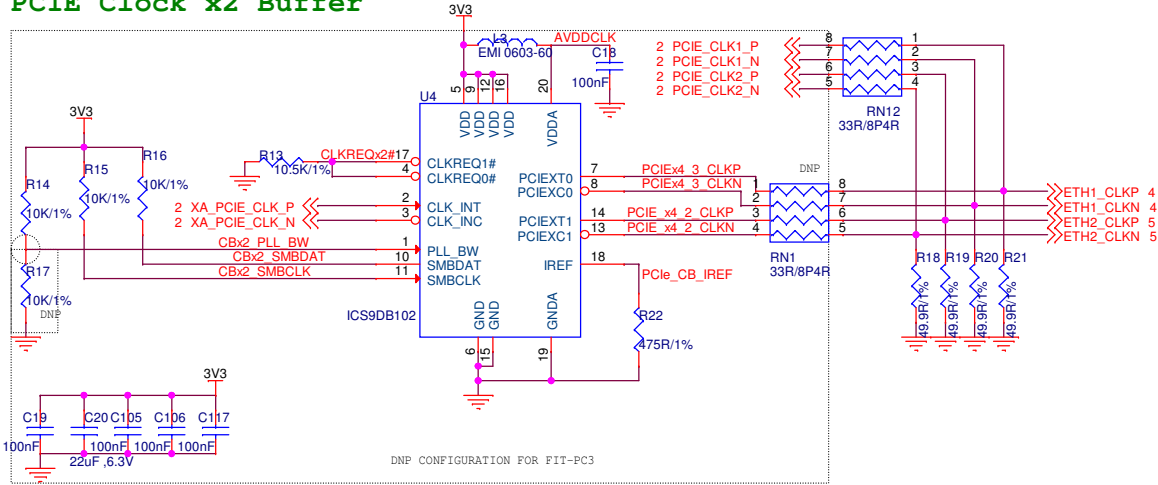


P5		RJ45nMAG 1G	
			RJ45 side
1	MDI0+	1	2
2	MDI0-	2	1
3	MDI1+	3	6
4	MDI1-	4	3
5	MDI2+	5	4
6	MDI2-	6	5
7	MDI3+	7	7
8	MDI3-	8	8
9	SHIELD		
10	SHIELD		

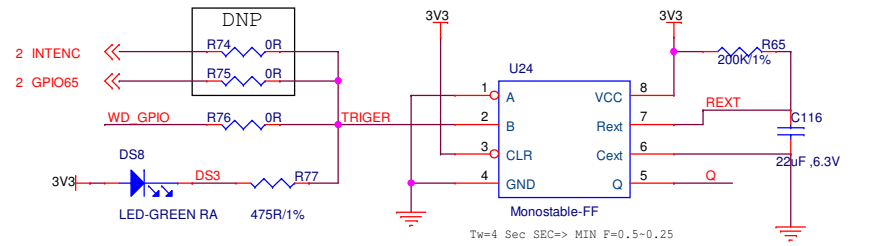
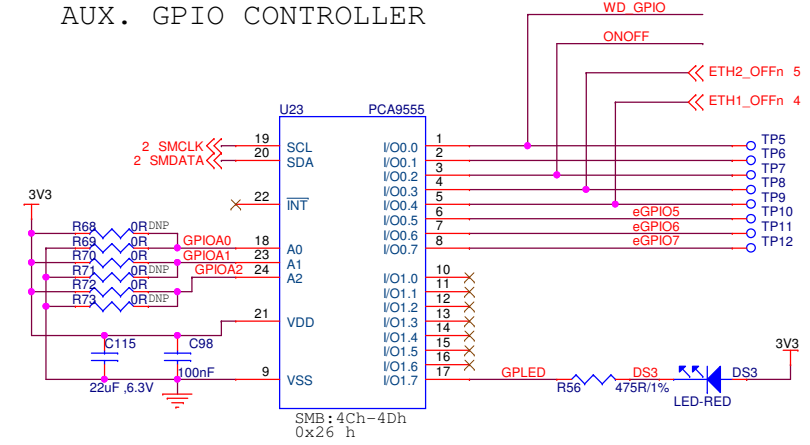


		RJ45 side	
1	MDI0+	1	2
2	MDI0-	2	1
3	MDI1+	3	6
4	MDI1-	4	3
5	MDI2+	5	4
6	MDI2-	6	5
7	MDI3+	7	7
8	MDI3-	8	8
9	SHIELD		
10	SHIELD		

PCIE Clock x2 Buffer



AUX. GPIO CONTROLLER



3V3_S0_S5 (3.3V, 3A)

